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back off algorithm

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maps the **backoff algorithm** into the p-persistent scheme. All existed work to our awareness post-busy slot leads the **backoff algorithm** deviate the p- ...
lion.cs.uiuc.edu/~chunyuhu/publications/chunyuhu-UIUCDCS-R-2005-2599-corr.pdf -
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CONTENTION RESOLUTION BACKOFF ALGORITHM

Many random access **algorithms** have been implemented, investigated and studied for a period of time in data communication industry.

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Science Links Japan | Complemental Back-off Algorithm for Merging ...

Abstract;A new complemental **back-off algorithm** for merging two N-gram language models is proposed. By merging several topic-dependent or style-dependent ...
[sciencelinks.jp/j-east/article/200219/000020021902A0753974.php](#) - 9k -
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NNRPD Exponential Backoff

Clearly an exponential **backoff algorithm** would be sufficient to allow me to post at my prolific rate, and still slow down a spammer's posting robot. ...

[www.jetcafe.org/dave/usenet/backoff.html](#) - 7k - [Cached](#) - [Similar pages](#) - [Note this](#)

backoff Resources on ZDNet

This contention window is dynamically controlled by the **Binary Exponential Backoff (BEB) algorithm**. The BEB... Tags: **Algorithm**, IEEE 802.11, ...
[updates.zdnet.com/tags/backoff.html](#) - 72k - [Cached](#) - [Similar pages](#) - [Note this](#)

Weighted hierarchical backoff algorithm for wireless ad hoc ...

hierarchical **backoff algorithm** to decrease the percentage of colli- The proposed hierarchical **backoff algorithm** employs two mecha- ...
[ieeexplore.ieee.org/iel5/7633/20836/00965966.pdf?arnumber=965966](#) -
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A Distributed Backoff Algorithm to support real-time traffic on ...

Ethernets are very popular in LANs because of their ease of administration. With the growing trend towards distributed multimedia applications, ...
[citeseer.ist.psu.edu/398437.html](#) - 21k - [Cached](#) - [Similar pages](#) - [Note this](#)

[PDF] Fibonacci Backoff Algorithm for Mobile Ad Hoc NetworksFile Format: PDF/Adobe Acrobat - [View as HTML](#)

In this paper, we present a new **backoff algorithm**, referred to as Fibonacci Increment **Backoff (FIB)**" that can overcome the ...
[www.dcs.gla.ac.uk/publications/PAPERS/8192/TR-2006-212.pdf](#) - [Similar pages](#) - [Note this](#)

IGI Global: Publication Details

The logarithmic **algorithm** proposes some improvements to the **backoff algorithms** that aim to efficiently use the channel and to reduce collisions. ...

www.igi-global.com/articles/details.asp?id=6629 - 48k - [Cached](#) - [Similar pages](#) - [Note this](#)

Upgrading and Repairing Networks - Google Books Result

by Terry William Ogletree - 2002 - Computers

The **Backoff Algorithm** Without a **backoff algorithm**, the device that detects a collision will stop and then try once again to transmit its data onto the ...
books.google.com/books?isbn=0789725576...

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back off (method OR algorithm) number (time OR attempt)

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Web Personalized Results 1 - 10 of about 58,600,000 for back off (method OR algorithm) number (time OR attempt)**Method to dynamically adjust the maximum back off time of an ...**

The **method** as described in claim 1, wherein (a) comprises: determining said maximum back off time limit based on said **number** of collisions said first data ...

www.patentstorm.us/patents/6606327-claims.html - 18k - [Cached](#) - [Similar pages](#) - [Note this](#)

Mechanism and method to select a time delay in response to a ...

The **method** of claim 1, wherein selecting the first time delay using the ... with a **number** of collisions, wherein the second **backoff algorithm** comprises an ...

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NNRPD Exponential Backoff

Clearly an exponential **backoff algorithm** would be sufficient to allow me to ... However, they also required a faster return to the 0 **backoff time** state. ...

www.jetcafe.org/dave/usenet/backoff.html - 7k - [Cached](#) - [Similar pages](#) - [Note this](#)

Backoff time evaluation method and apparatus for ethernet - Patent ...

The **method** as defined in claim 1 wherein: the step of selecting said a **backoff time** includes selecting a first retransmission **attempt backoff time** and a ...

www.freepatentsonline.com/EP0632620.html - 53k - [Cached](#) - [Similar pages](#) - [Note this](#)

Winbond's ethernet backoff algorithm

For the eleventh through sixteenth (and last) retransmission **attempt**, ... Instead, they have a fixed **back-off** schedule. That is, the **time** between say the ...

www.vf.utwente.nl/~ptdeboer/misc/winbond.html - 9k - [Cached](#) - [Similar pages](#) - [Note this](#)

A Distributed Backoff Algorithm to support real-time traffic on ...

A Distributed **Backoff Algorithm** to support real-time traffic on Ethernet ... 13 A new binary logarithmic arbitration **method** for Ethernet - Molle - 1994 ...

citeseer.ist.psu.edu/398437.html - 21k - [Cached](#) - [Similar pages](#) - [Note this](#)

A MODIFIED BACKOFF ALGORITHM FOR IEEE 802.11 DCF- BASED MAC ...

exponential **backoff** (BEB) **algorithm** for contention. resolution. At each transmission (including the first, **attempt** as well), the **backoff time** is uniformly ...

ieeexplore.ieee.org/iel5/9709/30647/01414683.pdf?arnumber=1414683 -

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LNCS 4325 - Performance Evaluation of Binary Negative-Exponential ...

BEB increases as the **number** of contending stations increases. 5 Conclusion. In this paper, we proposed a binary-negative exponential **backoff algorithm** to ...

www.springerlink.com/index/6t4l20p117jqr0t4.pdf - [Similar pages](#) - [Note this](#)

A Distributed Backoff Algorithm to support real-time traffic on ...

diately with probability 1/Q, where Q is the **number** of **backoff algorithm** for supporting real-time traffic on. 100 Mbps Ethernet. ...

portal.acm.org/citation.cfm?doid=383237.383243 - [Similar pages](#) - [Note this](#)

CiNii - Modified Backoff Algorithm with Station Number ...

The IEEE 802.11 WLAN standards adopt CSMA/CA protocol with a **backoff algorithm** as medium access control technique When the **number** of stations which **attempt** ...
ci.nii.ac.jp/naid/110003221605/en/ - 19k - [Cached](#) - [Similar pages](#) - [Note this](#)

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(generate OR calculate) (delay OR wait OR pa

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Apparatus for suspending the bus

cycle of a microprocessor by inserting **wait** ...

US Pat. 5581745 - Filed Dec 17, 1993 - Fujitsu Limited

When the **wait request** signal is detected, the bus cycle in the microprocessor
... generate wait requests to the main control part, the **wait request** from the ...

Cycle control for a microprocessor with multi-speed control stores

US Pat. 4366540 - Filed Feb 12, 1981 - International Business Machines Corporation

Latch 212 functions to **generate** the PAUSE RESPONSE signal for application ...

This interlock mechanism is implemented by use of the PAUSE REQUEST signal, ...

Method and apparatus for providing continuation of a communication call ...

US Pat. 5664007 - Filed Mar 6, 1995

The pause request signal would signaling links 468-470, respectively, ...
would generate a reconnection request and transmit covered by another network, ...

Method, apparatus, and computer program product for server bandwidth ...

US Pat. 6424993 - Filed May 26, 1999 - RespondTV, Inc.

5 may be used to **generate** delay values for each of the set of request handlers 6200.
Illustrative of the introduction of **delay** values in transmitting ...

Multiple processor synchronized halt test arrangement

US Pat. 4502116 - Filed Nov 17, 1982 - AT&T Bell Laboratories

... logic circuit means responsive to a generated pause request signal from ...
said on-line one of said central control means to **generate** a halt signal and ...

Vehicle navigation system

US Pat. 5311434 - Filed Jul 31, 1992 - Zexel Corporation

4 the operator should press the pause switch 71 of the pause request input ...
to the route calculator for having the route calculator calculate the route ...

Multiple time zone, alarm and user programmable custom watch

US Pat. 4283784 - Filed May 9, 1978 - Timex Corporation

12 in response to a plurality of delay request signals. There are three 5 second
delay ... 5 through 8 which in turn generate the various request signals. ...

Memory controller with integrated delay line circuitry

US Pat. 5276858 - Filed Dec 26, 1991 - Intel Corporation

processor and the second port receives a second request from one of the ...
wherein the delay line means does not **generate** the memory timing con- 20 trol ...

Delay locked loop with delay control unit for noise elimination

US Pat. 6489822 - Filed Dec 22, 2000 - Hynix Semiconductor Inc.

A delay locked loop, comprising: a delay means for delaying a clock signal fed

... to generate the **delay** control signal after a count of add **request** or ...

Pipe-lined data processor system of synchronous type having memory devices ...

US Pat. 5043883 - Filed Dec 16, 1988 - Hitachi, Ltd.

8 is a state transition diagram of the internal ory **wait request** signal during

... normal memory 50 **generate** the memory **wait request** 'signal during writing ...

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